

## HDMI Distribution Amplifiers

# KD-DA1x2DC

4K/18G 1x2 HDMI Distribution Amplifier / 4K to 1080p Down-converter with L/R and Optical Audio De-Embedding

### KEY FEATURES

- › **HDMI Signal Distribution:** 1 HDMI source to 2 HDMI outputs/displays
- › **Ultra HD/4K Support:** 4096x2160 or 3840x2160 24/25/30/60hz at 4:4:4 (signals up to 18Gbps bandwidth)
- › **HDCP Licensing:** Fully licensed and compatible with HDCP 2.2
- › **HDR10 and Dolby Vision®:** More life-like images through a greater range of luminance levels
- › **Resolution Support:** Supports all SD, HD, and VESA (VGA, SVGA, XGA, WXGA, SXGA, UXGA) up to 4096x2160p.
- › **4K to 1080p Down Convert:** When enabled, resolution conversion is applied to individual output(s) based on EDID from the connected display
- › **Deep Color Support:** Up to UHD/4K 30Hz 4:4:4/12 bits or 60Hz 4:4:4/8 bit
- › **Audio De-embedding:** Audio of the connected HDMI input is output at the digital Optical and analog 3.5mm stereo connectors.
- › **Full Buffer System™:** Manages TMDS re-clocking / signal re-generation, HDCP authentication to source & display, and EDID Control handshake
- › **EDID Management:** Internal library with 15 internal EDID handshakes including 4K with HDR in addition to native EDID data copied from output/display 1
- › **Digital Audio Format Support:** DTS-HD Master Audio™, DTS:X®, Dolby® TrueHD, Dolby Atmos®
- › **CEC Disconnect:** Prevents unwanted control of source



### SPECIFICATIONS

- › Input: HDMI Connector, Type A, 19 Pin Female
- › Output (Each): HDMI Connector, Type A, 19 Pin Female
- › Bandwidth: TMDS bandwidth 18 Gbps
- › DDC Signal (Data): Input DDC Signal - 5 Volts p-p (TTL)
- › HDMI Video/Audio Signal: Input Video Signal - 1.2 Volts p-p
- › Power Supply: KD-PS5V1ASC, 5V/1A, 100-240VAC, 50-60Hz, Interchangeable head, screw-in connector
- › Regulation: CE, RoHS, WEEE
- › Enclosure: Black Metal
- › Product Dimensions: 4.3" x 1.1" x 2.8"
- › Packaging Dimensions: 6.3" x 4.2" x 3.7"
- › Product Weight: 0.5 lb
- › Shipping Weight: 0.7 lb

### SYSTEM DESIGN EXAMPLE

